42-DL221

Version	: A.005
Issue Date	: 2004/03/02
File Name	: SP-DL221-A.005.doc
Total Pages	: 6

Optical Fiber Transmitting IC





42-DL221

OPTICAL FIBER TRANSMITTING IC

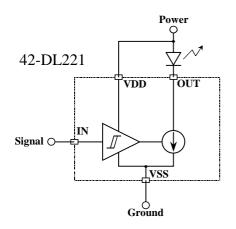
GENERAL DESCRIPTION

42-DL221 is a driver IC designed for the application of high-speed optical fiber transmission. It integrates the LED driver with constant current output to reduce the complexity and the cost of the transmission module. 42-DL221 can transmit with the speed up to 25Mb/s. 42-DL221 are fabricated by using CMOS technology with low power consumption purpose.

FEATURES

- 1. TTL interface compatible
- 2. High speed (up to 25Mb/s)
- 3. Uniform output waveform
- 4. Constant current output
- 5. Low power consumption
- 6. Wide range for Supply Voltage (2.7V-5.5V)

BLOCK DIAGRAM And APPLICATION CIRCUIT





ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Rating	Unit
Supply Voltage	V _{DD}	-0.5 to +6.5	V
Input Voltage	Vin	-0.5 to V _{DD} +0.5	V
Operating Temperature	Topr	-40 to +85	°C
Storage Temperature	Tstg	-55 to +100	°C
Electrostatic Damage	ESD	HBM class 3	
Output Voltage	Vout	-0.5 to V _{DD} +0.5	V

RECOMMENDED OPERATING CONDITIONS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	Vdd	2.7	-	5.5	V
High Level Input Voltage	V _{IH}	2	-	V _{DD}	V
Low Level Input Voltage	V _{IL}	0	-	0.8	V

ELECTRICAL CHARACTERISTICS (V_{DD}=5V, TA=25°C, if not mentioned)

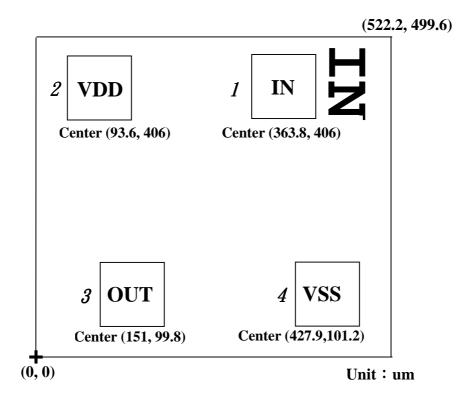
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ITEM	SYMBOL	CONDITIONS	MIN.	ТҮР.	MAX.	UNIT
High Level Input Voltage	V _{IH}	-	2.0	-	V _{DD}	V
Low Level Input Voltage	V _{IL}	-	0	-	0.8	V
Input Leakage Current	I _{IN}	VIN=VDD or VSS	-	-	1	uA
Quiescent Supply Current	I _{DDQ}	VIN =VDD	-	0.5	-	mA
Output Sinking Current	Iout_on	Vin=Vdd Vfled=1.8V	2.4	3	3.6	mA
Output bias Current	Ів	Vout= Vdd Vin=Vss	-	0.14	-	mA
Propagation Delay	T_{PLH} , T_{PHL}	VFLED=1.8V	-	-	40	ns
Rise Time, Fall Time of Iout	T _r , T _f	VFLED=1.8V	-	-	9	ns
Pulse Width Distortion	Δtw	Vfled=1.8V	-8	0	8	ns
Jitter of Output Current	∆tj	VFLED=1.8V		1	-	ns
Data Rate	Fdata	NRZ Code, Vfled=1.8V	-	-	25	Mb/s



PAD DESCRIPTIONS

PAD NO.	PAD NAME	DESCRIPTIONS
1	IN	Input Pad(Active High)
2	VDD	Supply Voltage
3	OUT	Output Pad Sinking Current(Active Low)
4	VSS	Ground

DIE CONFIGURATION



Die Size: 522.2*499.6um Die Thickness: 15mil(≒375um) Pad Size: 100um * 100um

* Note: SiTI reserves the right to improve the device geometry and manufacturing processes without prior notice. Though these improvements may result in slight geometry changes, they will not affect die electrical characteristics and pad layouts.



REQUIREMENTS FOR WAFER DELIVERY

Material :Silicon with P-SubstrateDiameter :6 inches(≒15cm)Thickness :15 mils(≒375um)Malfunctioned die :Marked with red ink or equivalent marking

HANDLING RECOMMENDATION FOR STATIC ELECTRICITY PROTECTION

- (1) Avoid any circumstance that produce static electricity, e.g. rubbing against plastic, during moving, storing and processing 42-DL221.
- (2) Process 42-DL221 in a clean room with proper temperature and humidity.
- (3) Ground all working machines and workers wear anti-electrostatic ring to ground during processing.
- (4) Avoid contact 42-DL221 with bare hands .If unavoided, wear anti-electrostatic ring and use anti-electrostatic tool to pick it up.

GUARANTED TEMPERATURE AND RETENTION CYCLE

(1) The device/wafer 42-DL221 should be stored in the nitrogenous chest. The conditions suggested are as follows:

Temperature = $23\pm3^{\circ}$ C

Relative Humidity = $50\pm10\%$

Minimum nitrogen inflow = 3 liters/minute

- (2) If the device/wafer, 42-DL221 is incidentally exposed to the air, use it for manufacturing as soon as possible.
- (3) Under the storage environment specified in item (1), six-month safe storage period is guaranteed.



The products listed herein are designed for ordinary electronic applications, such as electrical appliances, audio-visual equipment, communications devices and so on. Hence, it is advisable that the devices should not be used in medical instruments, surgical implants, aerospace machinery, nuclear power control systems, disaster/crime-prevention equipment and the like. Misusing those products may directly or indirectly endanger human life, or cause injury and property loss.

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